



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

BIBDATASHEET

CONFIRMATION NO. 8710

Bib Data Sheet

SERIAL NUMBER 10/760,358	FILING DATE 01/21/2004 RULE	CLASS 438	GROUP ART UNIT 2818	ATTORNEY DOCKET NO. 501.38505CX2
-----------------------------	---------------------------------------	--------------	------------------------	--

APPLICANTS

Naoki Yamamoto, Saitama, JAPAN;
 Yoshikazu Tanabe, Saitama, JAPAN;

** CONTINUING DATA ***** *Verified*

This application is a CON of 09/577,671 05/25/2000 PAT 6,737,341

** FOREIGN APPLICATIONS ***** *Verified*

JAPAN 11-158089 06/04/1999

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 04/27/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 27	TOTAL CLAIMS 13	INDEPENDENT CLAIMS 3
---	---	------------------------------	-------------------------	-----------------------	----------------------------

Verified and Acknowledged *[Signature]*
 Examiner's Signature Initials

ADDRESS
 020457
 ANTONELLI, TERRY, STOUT & KRAUS, LLP
 1300 NORTH SEVENTEENTH STREET
 SUITE 1800
 ARLINGTON, VA
 22209-9889

TITLE
 Semiconductor integrated circuit device and method for manufacturing the same

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees
RECEIVED	No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> 1.16 Fees (Filing)
	No. _____ for following:	<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
		<input type="checkbox"/> 1.18 Fees (Issue)